



CMKSH2-4LR
SURFACE MOUNT ULTRAmi™
TRIPLE ISOLATED SILICON
LOW V_F SCHOTTKY DIODES



Central™

Semiconductor Corp.

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMKSH2-4LR contains three (3) Isolated Silicon Low V_F Schottky Diodes, epoxy molded in a SOT-363 surface mount package. This ULTRAmi™ device has been designed for switching applications requiring a low forward voltage drop.

MARKING CODE: CHTL

MAXIMUM RATINGS: (T_A=25°C)

Peak Repetitive Reverse Voltage
Continuous Forward Current
Peak Repetitive Forward Current
Forward Surge Current, tp=10ms
Power Dissipation
Operating and Storage Junction Temperature
Thermal Resistance

SYMBOL

V _R RM	40
I _F	200
I _{FRM}	350
I _{FSM}	1.0
P _D	350
T _J , T _{stg}	-65 to +150
θ _{JA}	357

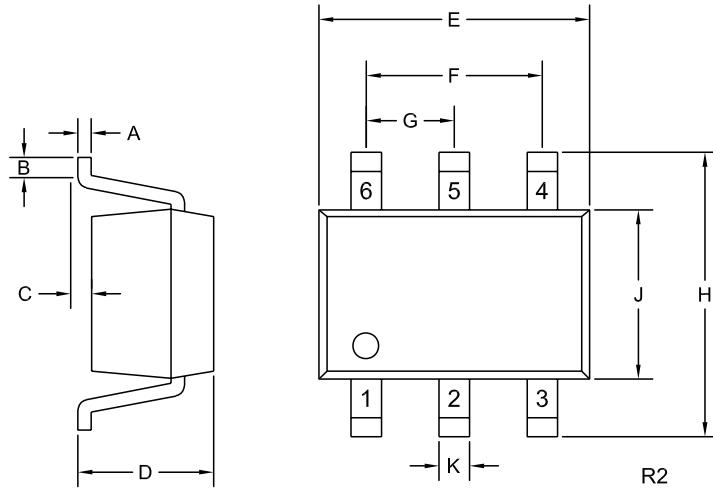
UNITS

V
mA
mA
A
mW
°C
°C/W

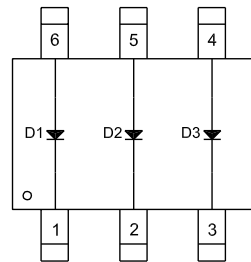
ELECTRICAL CHARACTERISTICS PER DIODE: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _R	V _R =20V		11	50	µA
BV _R	I _R =100µA	40	53		V
V _F	I _F =10mA		0.24	0.325	V
V _F	I _F =100mA		0.35	0.40	V
V _F	I _F =200mA		0.42	0.50	V
C _T	V _R =4.0V, f=1.0MHz		8.5	10	pF
t _{rr}	I _F =I _R =10mA, I _{rr} =1.0mA, R _L =100Ω		4.0	5.0	ns

SOT-363 CASE - MECHANICAL OUTLINE



PIN CONFIGURATION



LEAD CODE:

- 1) CATHODE D1
- 2) CATHODE D2
- 3) CATHODE D3
- 4) ANODE D3
- 5) ANODE D2
- 6) ANODE D1

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SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.004	0.010	0.10	0.25
B	0.005	-	0.12	-
C	0.000	0.004	0.00	0.10
D	0.031	0.043	0.80	1.10
E	0.071	0.087	1.80	2.20
F	0.051		1.30	
G	0.026		0.65	
H	0.075	0.091	1.90	2.30
J	0.043	0.055	1.10	1.40
K	0.006	0.012	0.15	0.30

SOT-363 (REV: R2)